



PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

Nathan R. Brown

Serial No.: 10/715,267

Filed: November 17, 2003

For: METHODS FOR POLISHING
SEMICONDUCTOR DEVICE
STRUCTURES BY DIFFERENTIALLY
APPLYING PRESSURE TO SUBSTRATES
THAT CARRY THE SEMICONDUCTOR
DEVICE STRUCTURES

Confirmation No.: Unknown

Examiner: Unknown

Group Art Unit: Unknown

Attorney Docket No.: 2269-4375.1US

CERTIFICATE OF MAILING

I hereby certify that this correspondence along with any attachments referred to or identified as being attached or enclosed is being deposited with the United States Postal Service as First Class Mail on the date of deposit shown below with sufficient postage and in an envelope addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

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Deidra J. Pfeil
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Commissioner for Patents
P.O. Box 1450
Alexandria, Virginia 22313-1450

Sir:

Prior to examination of the above-referenced patent application on the merits, entry of the amendments as set forth herein is respectfully solicited.

Amendments to the Specification appear on page 2 of this paper.

Amendments to the Claims are reflected in the listing which begins on page 3 of this paper.

Remarks begin on page 8 of this paper.